HercuLeS feature matrix and pricing schemes

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Author: Nikolaos Kavvadias
Email: nkavvadias@ajaxcompilers.com
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HercuLeS feature matrix

Feature	Freemium	Basic	Full
HercuLeS GUI	Y	Y	Y
Automatic RTL VHDL code generation	Y	Y	Y
Automatic testbench generation	Y	Y	Y
Portable vendor-independent VHDL code generation	Y	Y	Y
Automatic user-defined IP integration	Y	Y	Y
Graph-based synthesis engine	Y	Y	Y
Generation of compiler graphs (call graphs, control flow graphs, control-data flow graphs)	Y	Y	Y
C frontend	Y	Y	Y
GHDL support	Y	Y	Y
Mentor Modelsim support	Y	Y	Y
Email support	Y	Y	Y
Phone and Skype support	Y	Y	Y
Live/on-site consultancy support	Y	Y	Y
Parallel operation scheduling		Y	Y
Automatic block RAM inference		Y	Y
Constant multiplication optimization		Y	Y
Constant division optimization		Y	Y
VHDL-2008 floating-point arithmetic support		Y	Y
C verification backend		Y	Y
Additional complete examples		Y	Y
Xilinx Isim support		Y	Y
VHDL-2008 fixed-point arithmetic support			Y

Register optimization			**Y**
Operation scheduling improvements			**Y**
Array flattening optimizations			**Y**
C source code optimizer			**Y**
Active-HDL support			**Y**

Pricing

Cost	Freemium	Basic	Full
Licensing (one-off fee)	FREE	1995.00 EUR	3995.00 EUR
Yearly maintenance	FREE	995.00 EUR	1995.00 EUR
Email consultancy fee	FREE	FREE	FREE
Phone consultancy fee (per hour)	95.00 EUR	75.00 EUR	49.00 EUR
On-site consultancy fee (daily; food, accomodation, travel not included)	495.00 EUR	395.00 EUR	295.00 EUR